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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	10/766,774	Confirmation No.	8283
First Inventor	Richard K. Williams	Filing Date	01/28/2004
Tech. Center/ Art Unit	2822	Examiner	Mary A. Wilczewski
Title:	Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology		
Docket No.:	AAT007-3C US	Customer No.:	34036

Santa Clara, California
October 25, 2005

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated July 27, 2005, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.